

N2H487 **32 bit ARM Cortex-M4F** **240MHz** **DSP**
512KB **Flash** **192KB SRAM** **32KB CCM SRAM** **+ 4KB Backup SRAM** **4** **12bit**
1Msp/s ADC **2** **12bit DAC** **USB FS Device** **USB HS DualRole** **U(S)ART** **I2C** **SPI** **CAN-FD**
Ethernet **DVP** **SDIO** **FEMC** **xSPI** **I2S**
 AES/TDES **SHA**
MD3 **SM4** **MD5** **TRNG**

SWD/JTAG

USB UART Bootloader

Flash

SMPU

WRP

RDP

L0/L1/L2

AES/TDES SHA SM3 SM4 MD5

TRNG

CRC16/32

96 UID 128 UCID

1.8V~3.6V

-40 105

ESD ±4KV HBM , ±1KV CDM

EFT VDD +/-4KV A I/O +/-2KV A

LQFP64(10mm x 10mm)

LQFP100(14mm x 14mm)

LQFP144(20mm x 20mm)

N32H487xE	N32H487REL7, N32H487VEL7, N32H487ZEL7

[illegible]

2

Ia69Auaa4a170ThGQ16A75HGMWu4FAHBCeCbA7er)5Ccr

		N32H487REL7	N32H487VEL7	N32H487ZEL7
		1.8~3.6V/-40~105		
CPU		ARM Cortex-M4F @240MHz, 300DMIPS		
Flash	KB	512	512	512
Total SRAM (KB)	General SRAM	160	160	160
	CCM SRAM ⁽¹⁾	32		
	Backup SRAM	4		
	ATIM	3*16bit		
	GTIM	7*16bit 3*16bit ⁽²⁾		
	BTIM	2*32bit		
	LPTIM	2*16bit		
	SysTick timer	1		
	WWDG	1*14bit		
	IWDG	1*12bit		
	RTC	Yes		

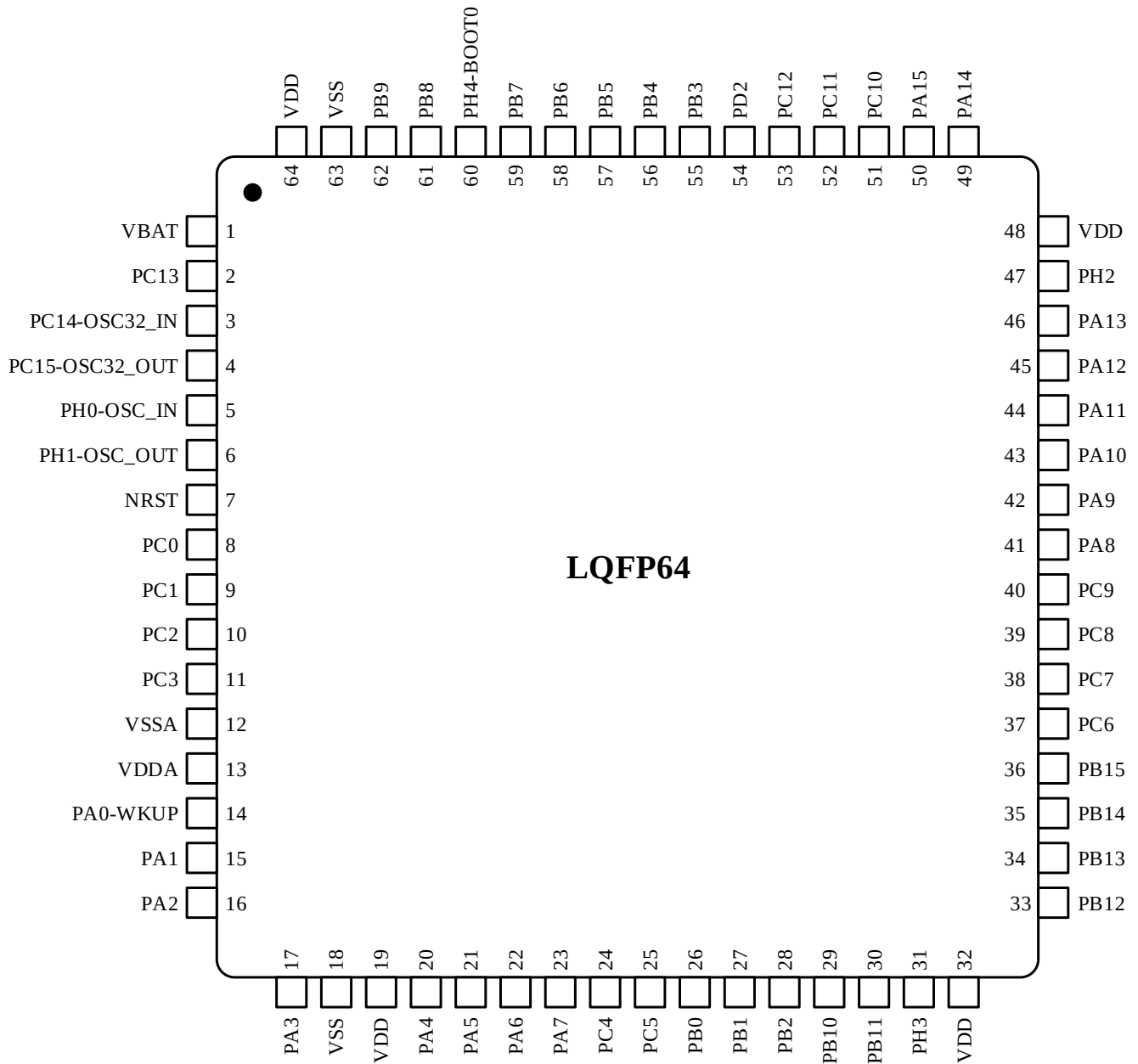
Cordic	Yes		
FMAC	Yes		
	RDP/WRP		
	LQFP64	LQFP100	LQFP144

1. CCM SRAM SRAM CCM SRAM
2. 1
- 3.

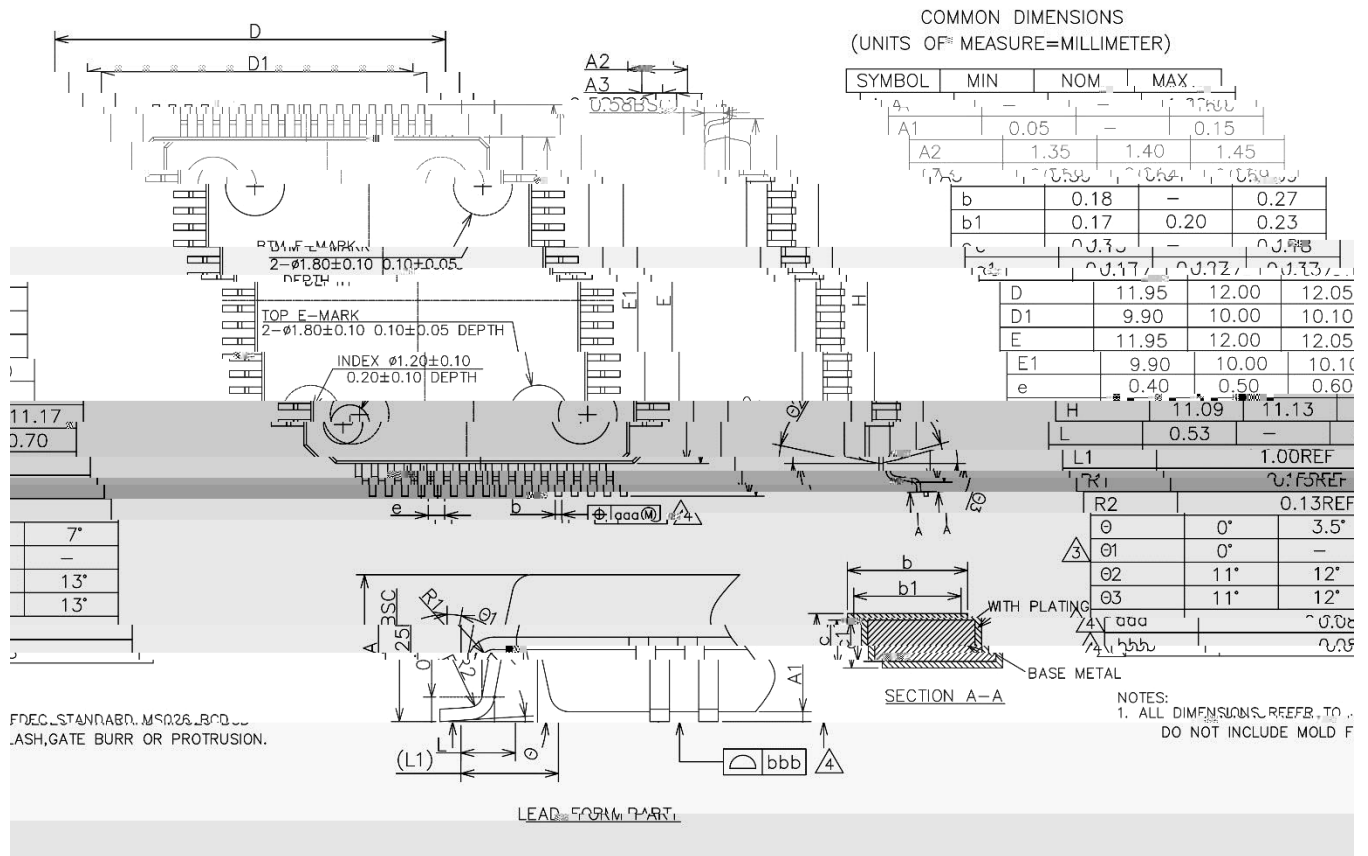
3

3.1

3.1.1

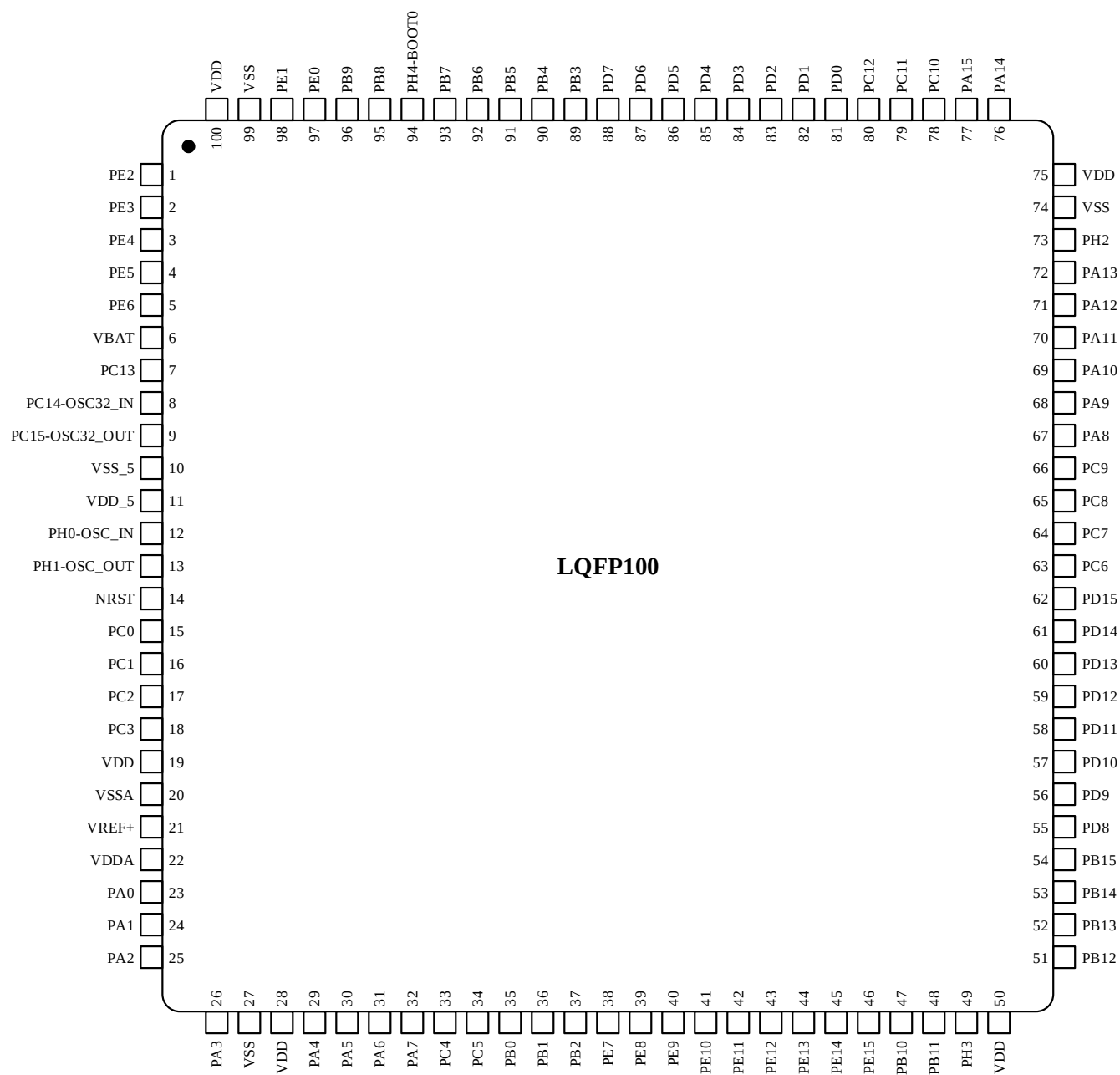


3.1.2

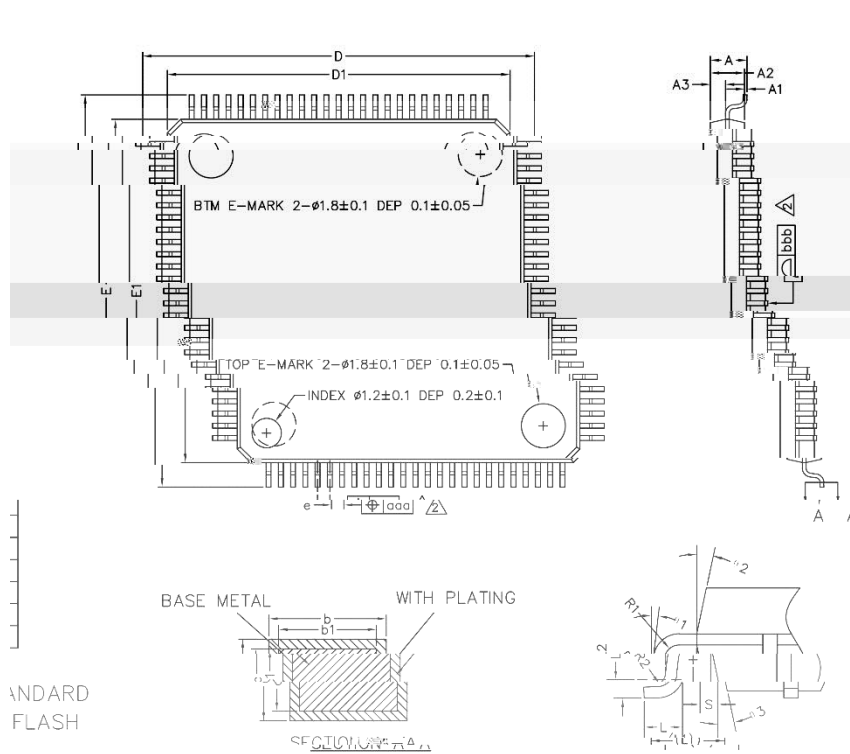


3.2

3.2.1



3.2.2



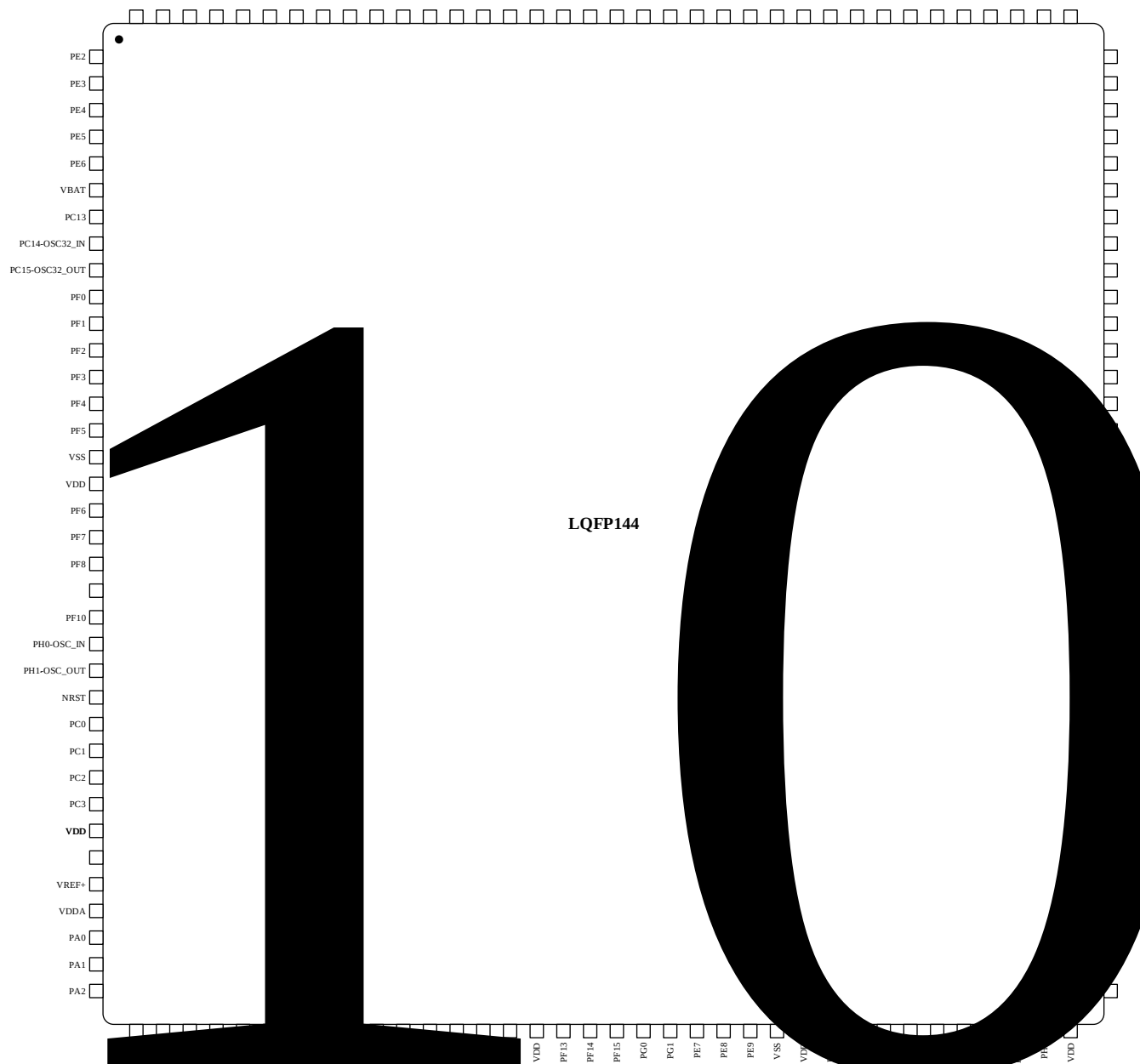
COMMON DIMENSIONS
(UNITS OF MEASURE=MILLIMETER)

SYMBOL	MIN	NOM	MAX
A	—	—	1.60
A1	0.05	—	0.15
A2	1.35	1.40	1.45
A3	0.59	0.64	0.69
b	0.17	—	0.27
b1	0.17	0.20	0.23
c	0.13	—	0.18
c1	0.12	0.127	0.134
D	13.90	14.00	14.10
D1	13.90	14.00	14.10
E	13.90	14.00	14.10
E1	13.90	14.00	14.10
L	0.45	0.60	0.75
L1	—	1.00REF	—
L2	—	0.25BSC	—
R1	0.08	—	—
R2	0.08	—	0.20
S	0.20	—	—
θ	0°	3.5°	7°
θ 1	0°	—	—
θ 2	11°	12°	13°
θ 3	11°	12°	13°
aaa	—	0.08	—
bbb	—	0.08	—

NOTES:
ALL DIMENSIONS REFER TO JEDEC ST. MS-026 BED DO NOT INCLUDE MOLD REPROJECTIONS.

3.3

3.3.1



3.3.2

